



Form PT-87449		U.S. Dept. of Commerce Patent & Trademark Office		Atty. Docket: 02-CT-099/DP		Serial No. 10/622,835	
List of Documents Cited by Applicant <small>(Use several sheets if necessary)</small>				Applicant: Francesco PAPPALARDO et al.			
				Filing Date: July 18, 2003			
Group: 2183							
<b>U.S. PATENT DOCUMENTS</b>							
Ex's In'tl		Document Number	Date	Name	Class	Sub-class	Filing Date, if applicable
JTS	AA1	5,392,423	February 21, 1995	Yetter			
JTS	AA2	5,870,899	September 23, 1997	Kohdaka			
JTS	AA3	6,122,751	September 19, 2000	Janassens et al.			
<b>FOREIGN PATENT DOCUMENTS</b>							
		Document Number	Date	Country	Class	Sub-class	Trans'n Yes/No
<b>OTHER DOCUMENTS</b> (Including Author, Title, Date, Pertinent Pages, Etc.)							
JTS	AA4	Moshnyaga, V.G. et al., "Energy Saving Techniques for Architecture Design of Portable Embedded Devices," ASIC Conference and Exhibit 1997 Proceedings, Tenth Annual IEEE, International Portland, OR, USA 7-10 September 1997, New York, NY IEEE September 7, 1997, XP010243383, pp. 163-167, ISBN: 0-7803-4283-6.					
JTS	AA5	European Search Report for application no. EP 02 42 5469 dated July 1, 2003.					
Examiner:				Date Considered:			
JTS				11/18/2005			
EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							